

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	("20020097366").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2004/12/16 20:39
S2	1	adherent ADJ member and (("20020097366").PN.)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/18 15:29
S3	1	adher\$4 ADJ member and (("20020097366").PN.)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/18 15:30
S4	1	adher\$4 and (("20020097366").PN.)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/18 16:26
S5	1	second ADJ conduct\$3 and (("20020097366").PN.)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/18 16:51
S6	12	shrink\$3 ADJ (PCB print\$3 ADJ circuit ADJ board)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 10:38
S7	266	(PCB print\$3 ADJ circuit ADJ board) with shrink\$3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/18 16:55
S8	502	349/152	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/18 16:56
S9	0	((PCB print\$3 ADJ circuit ADJ board) with shrink\$3) and 349/152	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/18 16:56
S10	383	349/150	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/18 16:56
S11	1	((PCB print\$3 ADJ circuit ADJ board) with shrink\$3) and 349/150	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/12/16 20:41
S12	142525	liquid ADJ crystal ADJ display	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/18 16:58
S13	6	(liquid ADJ crystal ADJ display) and ((PCB print\$3 ADJ circuit ADJ board) with shrink\$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/18 17:02
S14	1	thermal ADJ react\$3 and (("20020097366").PN.)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 10:31

S15	1	thermal and (("20020097366").PN.)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/18 17:25
S16	1	interval and first ADJ conduct\$3 and (("20020097366").PN.)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/18 17:35
S17	1	color and (("20020097366"). PN.)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/18 17:35
S18	111730	thermal ADJ expan\$4	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 10:31
S19	1656	(PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 10:40
S20	686	(equal identical) ADJ (thermal ADJ expan\$4)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 10:40
S21	0	((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP)) and ((equal identical) ADJ (thermal ADJ expan\$4))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 10:40
S22	5	((thermal ADJ expan\$4) same (equal identical)) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 11:33
S23	74	(equal identical) and ((thermal ADJ expan\$4) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP)))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 11:33
S24	69	((equal identical) and ((thermal ADJ expan\$4) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP)))) not (((thermal ADJ expan\$4) same (equal identical)) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP)))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 10:46
S25	16	(shrink\$3) and ((thermal ADJ expan\$4) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP)))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 11:33

S26	9	((equal identical) and ((shrink\$3) and ((thermal ADJ expan\$4) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP))))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 11:34
S27	0	((equal identical) and ((shrink\$3) and ((thermal ADJ expan\$4) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP)))) not (((thermal ADJ expan\$4) same (equal identical)) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP))) ((equal identical) and ((thermal ADJ expan\$4) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP)))) not ((thermal ADJ expan\$4) same (equal identical)) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP))))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 11:59
S28	7	((shrink\$3) and ((thermal ADJ expan\$4) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP)))) not (((thermal ADJ expan\$4) same (equal identical)) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP))) ((equal identical) and ((thermal ADJ expan\$4) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP)))) not ((thermal ADJ expan\$4) same (equal identical)) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP))))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 11:42
S29	11	mismatch and (thermal ADJ expan\$4) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 11:43

S30	6	(mismatch and (thermal ADJ expan\$4) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP))) not ((((thermal ADJ expan \$4) same (equal identical)) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP))) (((equal identical) and ((thermal ADJ expan\$4) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP)))))) not (((thermal ADJ expan \$4) same (equal identical)) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP)))) ((equal identical) and ((shrink\$3) and ((thermal ADJ expan \$4) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP))))))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 12:08
S31	8894	thermo?compress\$3 thermocompress\$3 thermo ADJ compress\$3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 12:09
S32	30	(((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP))) and ((thermo?compress\$3 thermocompress\$3 thermo ADJ compress\$3) and temperature)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 12:20
S33	848	(thermo?compress\$3 thermocompress\$3 thermo ADJ compress\$3) ADJ bond \$3 with temperature	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 12:20
S34	8	((thermo?compress\$3 thermocompress\$3 thermo ADJ compress\$3) ADJ bond \$3 with temperature) and ((PCB print\$3 ADJ circuit ADJ board) and (tape ADJ carrier ADJ package TCP))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/19 12:20
S35	1	("20020097366").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2004/02/20 10:06

S36	0	((("20020097366").PN.) and (gate and data) ADJ line and light ADJ shield	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/20 10:07
S37	0	((("20020097366").PN.) and (gate and data) ADJ line) and shield	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/20 10:07
S38	0	((("20020097366").PN.) and (gate and data) ADJ line) and light ADJ shield\$3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/20 10:08
S39	0	((("20020097366").PN.) and (gate and data) ADJ line) and shield\$3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/20 10:08
S40	1	((("20020097366").PN.) and (gate and data) ADJ line	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/02/20 10:08
S41	0	("(PCBwithbond\$3)").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2004/08/26 11:07
S42	2151	PCB with bond\$3	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2004/08/26 11:08
S43	46	(PCB and LCD) with bond\$3	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2004/08/26 11:12
S44	1	(PCB and LCD) with ((bond \$3 adhe\$4) with (thermocompress\$3 thermo?compress\$3 thermo ADJ compress\$3))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2004/08/26 11:15
S45	1	(PCB and LCD) same ((bond\$3 adhe\$4) with (thermocompress\$3 thermo?compress\$3 thermo ADJ compress\$3))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2004/08/26 11:15
S46	22	((print\$3 ADJ circuit ADJ board PCB) and (liquid ADJ crystal ADJ display LCD)) same ((bond\$3 adhe\$4) with (thermocompress\$3 thermo?compress\$3 thermo ADJ compress\$3))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2004/08/26 11:17
S47	2075	455/566	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2004/08/26 13:00
S48	25	(PCB and LCD) and 455/566	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2004/08/26 13:04
S49	1542	349/149	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2004/08/26 13:05

S50	52	349/149 and phone	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2004/08/26 13:05
S51	52	((349/149 and phone) not (PCB and LCD) and 455/566)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2004/08/26 13:05
S52	17923	(lead terminal land ADJ group) with interval	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2004/12/16 20:41
S53	142295	(PCB print\$3 ADJ circuit ADJ board)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/12/16 20:42
S54	702	S52 and S53	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/12/16 20:42
S55	18	S54 and external ADJ device	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/12/16 20:42
S56	37	(shrunk shrink\$3) ADJ (printed circuit board PCB)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 12:43
S57	47	(shrunk shrink\$3) ADJ (printed ADJ circuit ADJ board PCB)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 12:45
S58	2	S57 and TCP	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 12:50
S59	67631	(printed ADJ circuit ADJ board PCB) with (lead terminal land pad)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 12:54
S60	7638	(TCP tape ADJ carrier ADJ package) with (lead terminal land pad)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 12:56
S61	82	((printed ADJ circuit ADJ board PCB) with (lead terminal land pad)) with (shrunk shrink\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 12:57

S62	3	S60 and S61	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 12:57
S63	649	S59 and S60	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 12:58
S64	22	(shrunk shrink\$3) and S63	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 12:59
S65	14	(shrunk shrink\$3) ADJ (printed ADJ circuit ADJ board PCB) with (lead terminal land pad)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 13:32
S66	68	S61 not S57	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 13:33
S67	2	(TCP tape ADJ carrier ADJ package) and S66	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 13:34
S68	92	((printed ADJ circuit ADJ board PCB) with (lead terminal land pad connector)) with (shrunk shrink\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 13:38
S69	390	((printed ADJ circuit ADJ board PCB) with (lead terminal land pad connector)) with (thermal ADJ expansion)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 13:40
S70	8293	(TCP tape ADJ carrier ADJ package) with (lead terminal land pad connector)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 13:40
S71	18	S69 and S70	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 13:40

S72	2	("7019714").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/09/09 13:45
S73	1	10/706050	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/09 13:48

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